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PATENT

Attorney Docket No. IGARA31.001AUS

Date: January 2, 2002

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ATTENTION: BOX PATENT APPLICATION

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): 1) Daeyoul Yoon

2) Hiroshi Matsuura

For:

VARIABLE GROUP DELAY UNIT AND VARIABLE GROUP DELAY OPTICAL FIBER MODULE

(X) This application claims priority under 35 U.S.C. §119 to Japanese Patent Application number 2001-034092, filed February 9, 2001.

Enclosed are:

(X) Eight sheets of drawings.

(X) Certified Priority Document for Japanese Patent Application having application number 2001-034092, filed February 9, 2001.

(X) Return prepaid postcard.

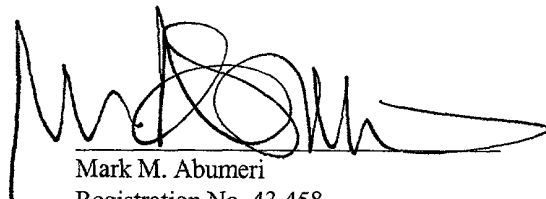
CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
Basic Fee			\$740	\$740
Total Claims	13 - 20 =	0 ×	\$18	\$0
Independent Claims	1 - 3 =	0 ×	\$84	\$0

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Mark M. Abumeri
Registration No. 43,458
Attorney of Record

Knobbe Martens Olson & Bear LLP

Intellectual Property Law

550 West C Street
Suite 1200
San Diego CA 92101
Tel 619-235-8550
Fax 619-235-0176
www.kmob.com

Mark M. Abumeri
mabumeri@kmob.com

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

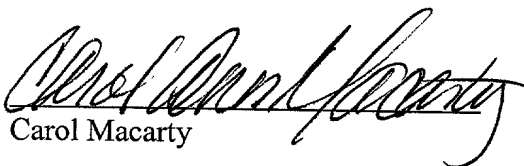
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Attorney Docket No. : IGARA31.001AUS
Applicant(s) : Yoon, et al.
For : VARIABLE GROUP DELAY UNIT AND
VARIABLE GROUP DELAY OPTICAL
FIBER MODULE
Attorney : Mark M. Abumeri
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Date of Deposit : January 2, 2002

I hereby certify that the accompanying

Transmittal letter; specification in thirty-seven (37) pages; eight (8) sheets of drawings; Certified Priority Document for Japanese Patent Application No. 2001-034092; Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.


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